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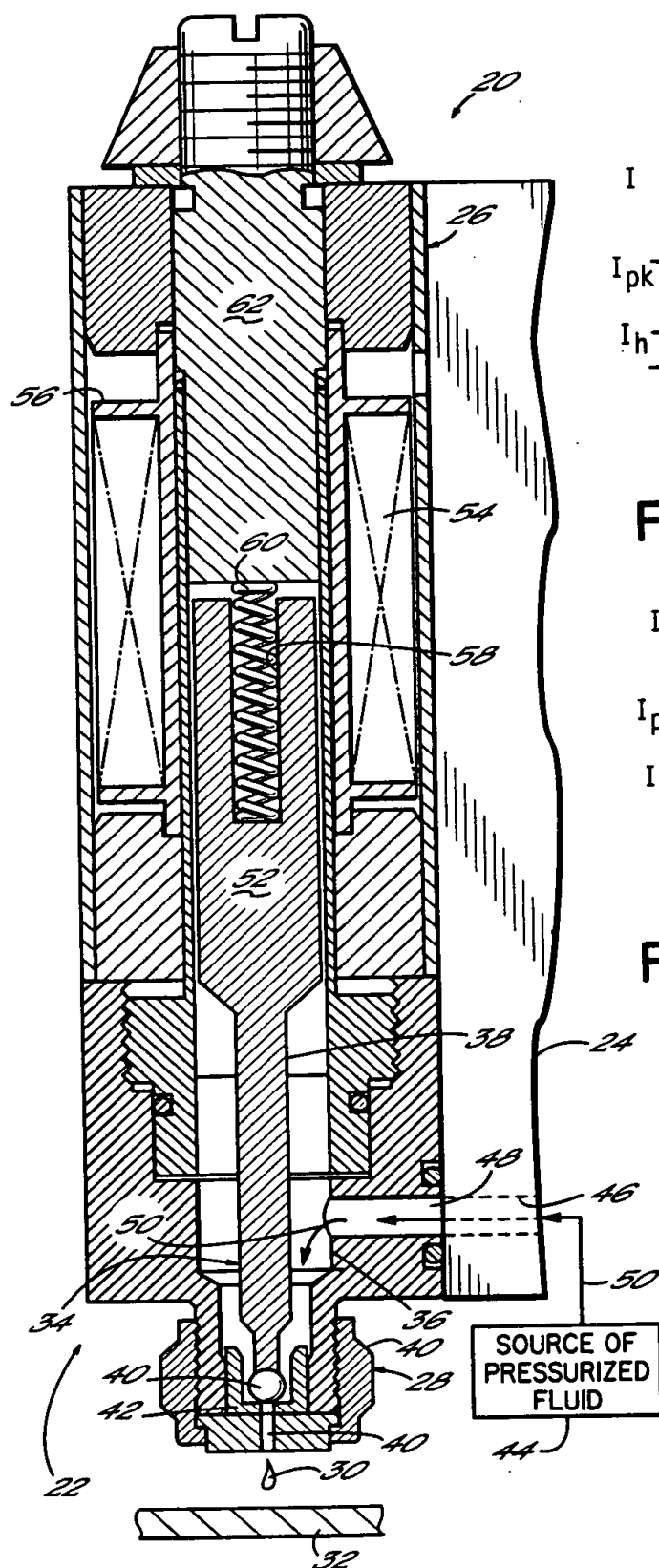


FIG. 1

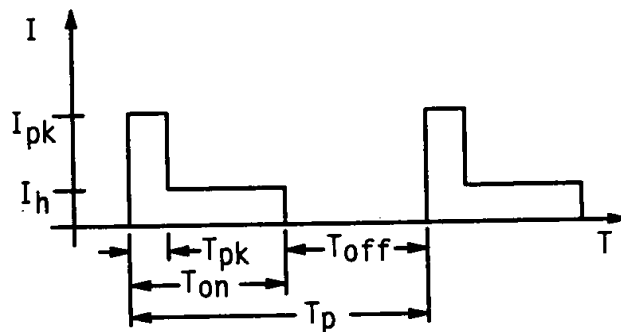


FIG. 2

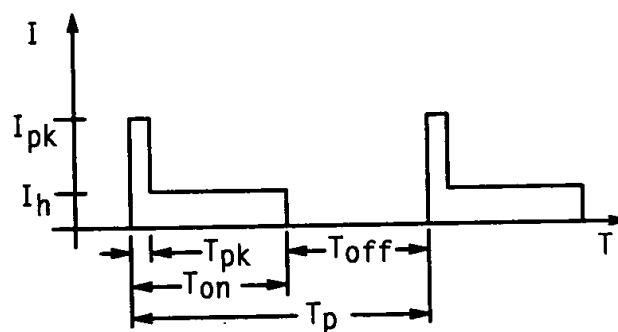


FIG. 2A

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Block diagram of a driver circuit 70. The circuit includes a POWER SUPPLY 72, a SYSTEM CONTROL block 80, a PEAK CURRENT DURATION CONTROL block 84, a PWM CONTROL block 86, a PWM block 130, an OSC block 131, a POWER SWITCH CIRCUIT block 74, a CURRENT SENSOR block 78, and an inductor 54. The POWER SUPPLY 72 is connected to the PEAK CURRENT DURATION CONTROL block 84 and the PWM CONTROL block 86. The SYSTEM CONTROL block 80 is connected to the PWM CONTROL block 86 and the CURRENT SENSOR block 78. The OSC block 131 is connected to the PWM block 130. The PWM CONTROL block 86 is connected to the OSC block 131 and the PWM block 130. The PWM block 130 is connected to the POWER SWITCH CIRCUIT block 74. The POWER SWITCH CIRCUIT block 74 is connected to the inductor 54. The CURRENT SENSOR block 78 is connected to the inductor 54 and ground. The entire circuit is enclosed in a dashed box labeled DRIVER CIRCUIT 70.

**FIG. 3**

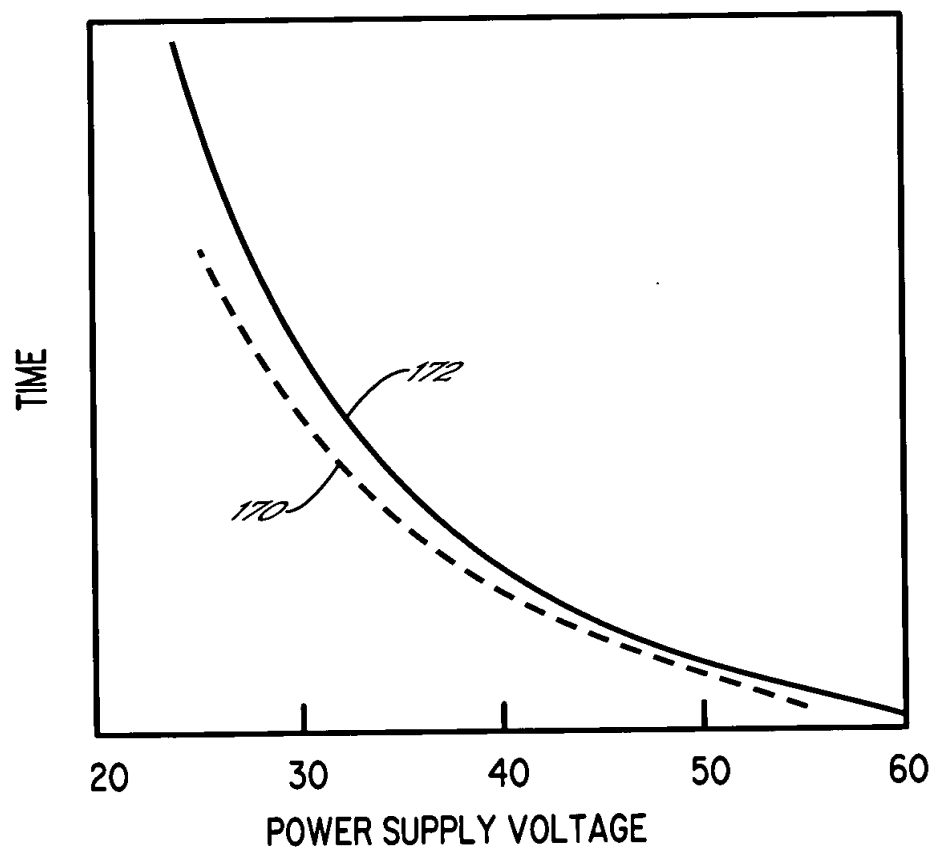


FIG. 5

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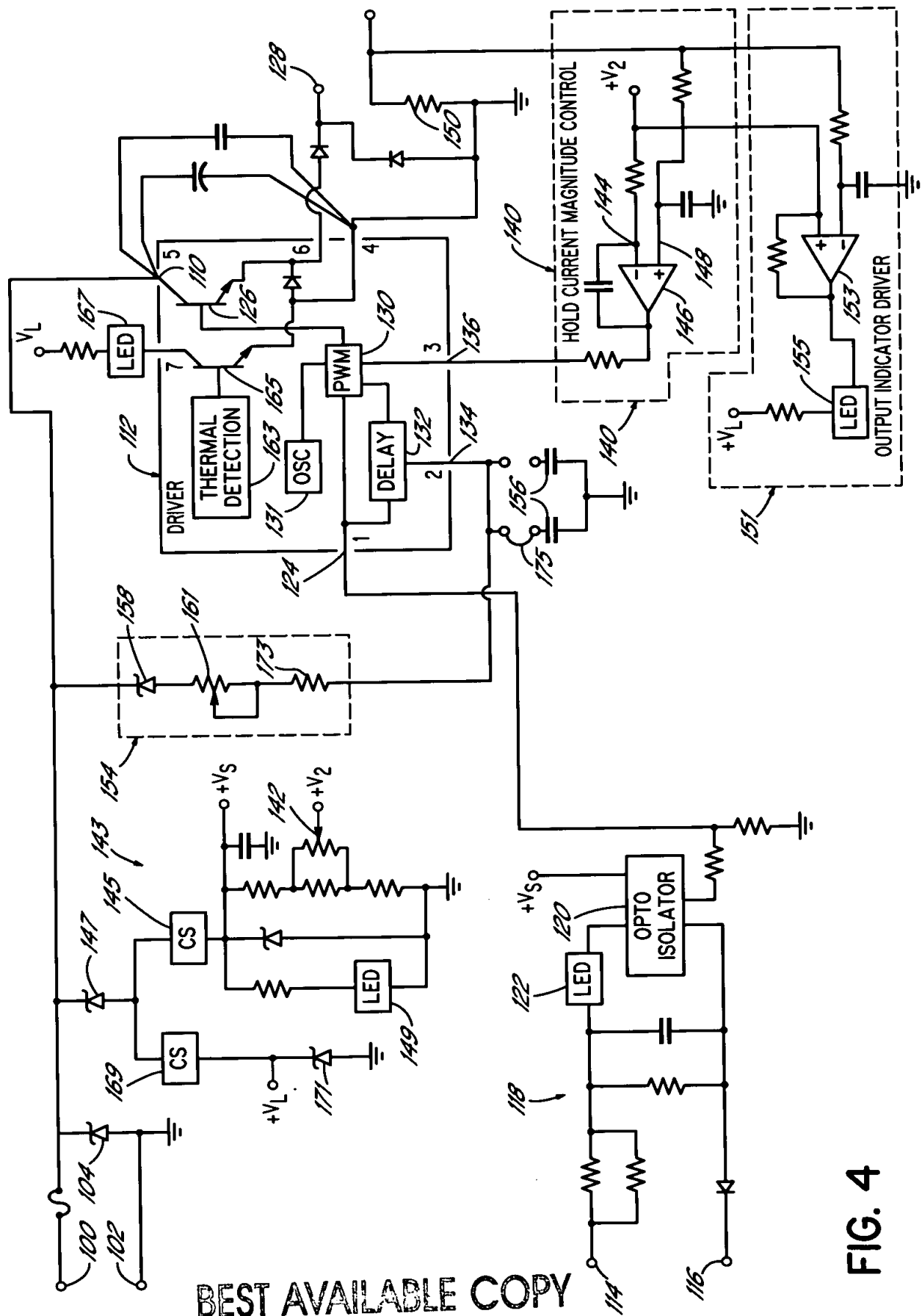
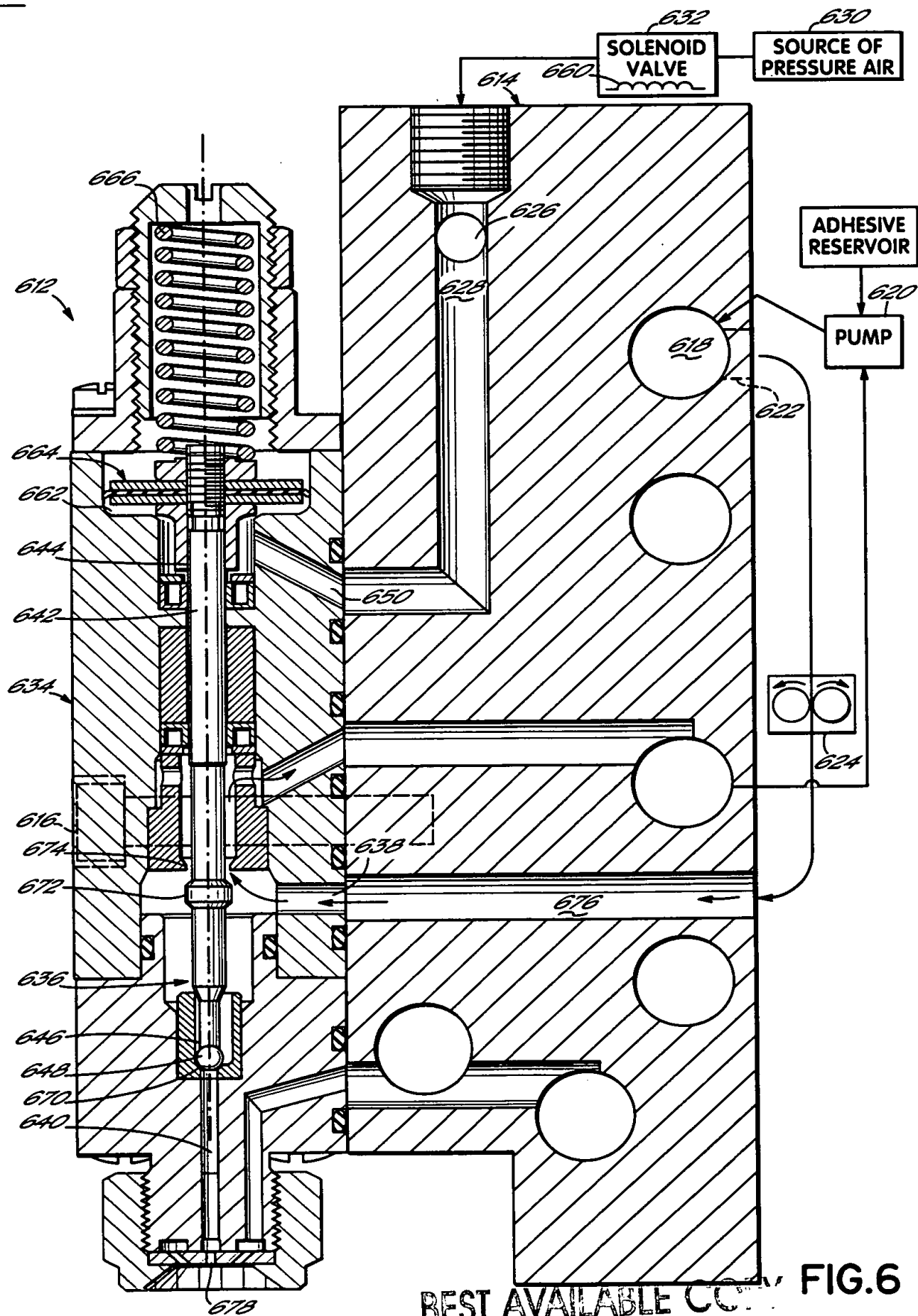


FIG. 4

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